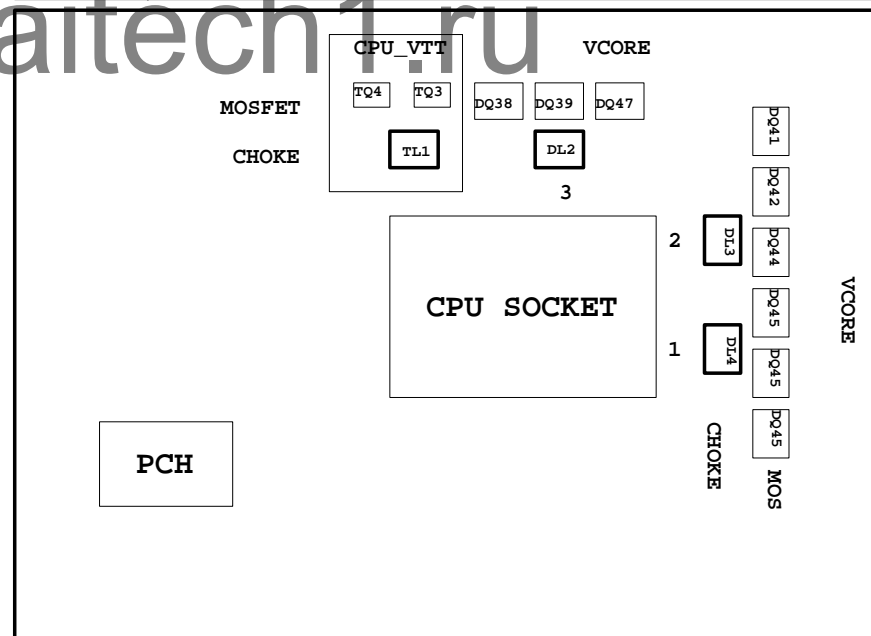


1.0

SHEET	TITLE
01	COVER SHEET
02	BOM & PCB MODIFY HISTORY
03	BLOCK DIAGRAM
04	CPU_LGA1155-A
05	CPU_LGA1155-B
06	CPU_LGA1155-C
07	DDR III CHANNEL A
08	DDR III CHANNEL B
09	PCH_FDI,DMI,USB,PCIE
10	PCH_DP,CLK BUFFER
11	PCH_HOST,SATA,PCI
12	PCH_GPIO,CTRL,AUDIO
13	PCH_PWR,GND
14	PCI EXPRESS*16 SLOT
15	PCI EXPRESSX4 SLOT / PCIE X1 SLOT
16	PCI SLOT 1~4
17	I/O ITE8728
18	COM, LPT, TPM
19	Dual BIOS
20	ALC887-VD2
21	REAR AUDIO JACK
22	VCORE_ ISL95836_1
23	VCORE_ ISL95836_2
24	DISCRETE POWER
25	PCH CORE / VOLTAGE CONSOLE
26	RT8120_CPU_VTT
27	VCCSA POWER

SHEET	TITLE
28	F_PANEL , F_USB
29	ATX POWER, CLOCK GEN
30	HWM,KB/MS , FAN CTRL
31	REALTEK RTL8111F-VL
32	mSATA
33	DDR / M3 POWER
34	
35	
36	
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38	
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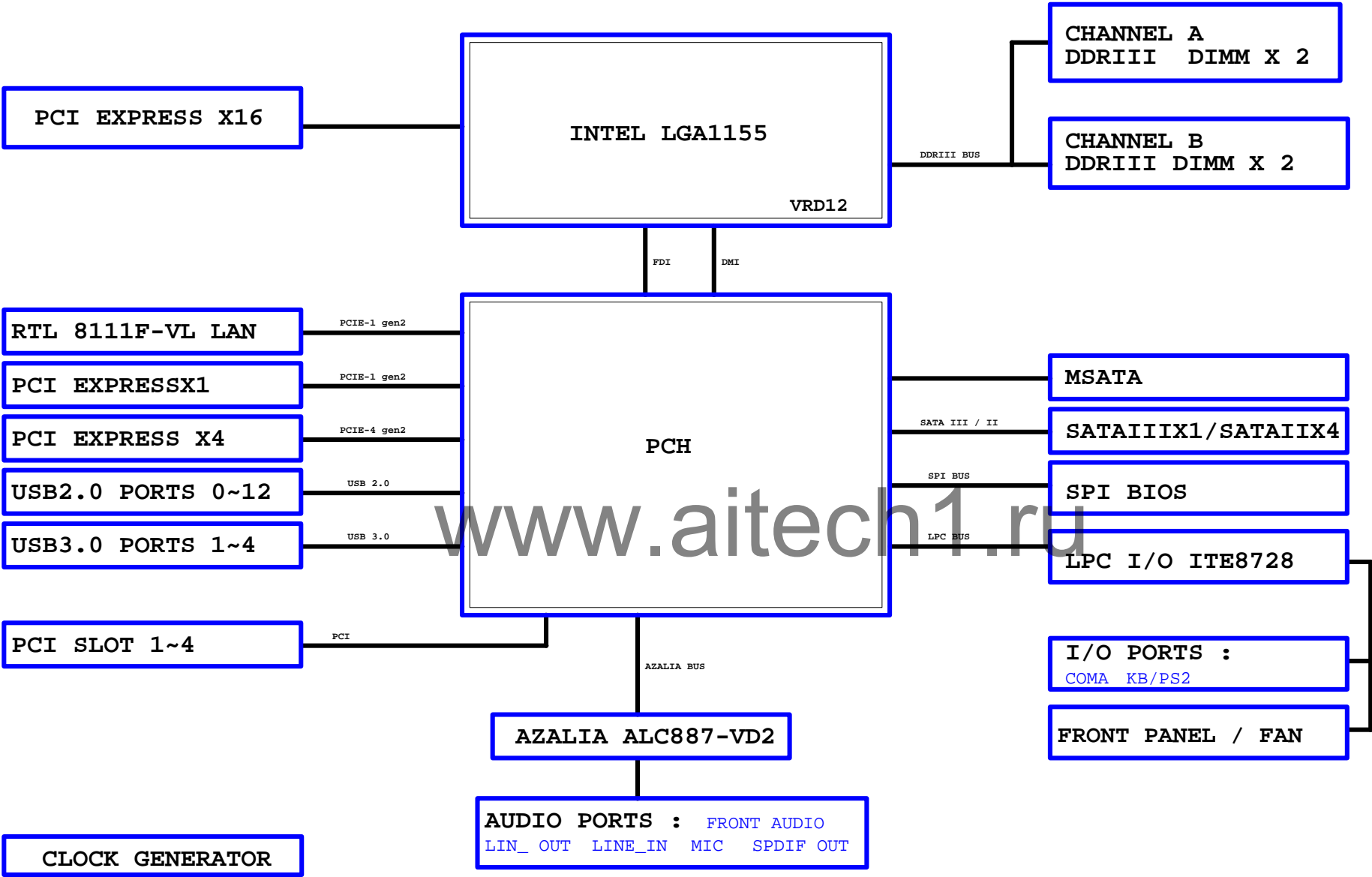


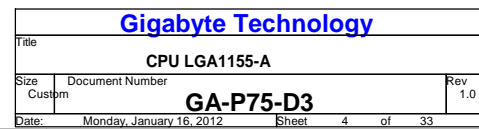
Component value change history

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[illegible]

BLOCK DIAGRAM





LGA1155A

MAAA0	AV27	SA_MA[0]	SA_DQS[0]	AK3	DQSA0
MAAA1	AY24	SA_MA[1]	SA_DQS[0]	AK2	-DQSA0
MAAA2	AW24	SA_MA[2]			
MAAA3	AW23	SA_MA[3]			
MAAA4	AV23	SA_MA[4]	SA_DQ[0]	AJ3	MDA0
MAAA5	AT24	SA_MA[5]	SA_DQ[1]	AJ4	MDA1
MAAA6	AT23	SA_MA[6]	SA_DQ[2]	AL3	MDA2
MAAA7	AU22	SA_MA[7]	SA_DQ[3]	AL4	MDA3
MAAA8	AV22	SA_MA[8]	SA_DQ[4]	AJ2	MDA4
MAAA9	AT22	SA_MA[9]	SA_DQ[5]	AJ1	MDA5
MAAA10	AV28	SA_MA[10]	SA_DQ[6]	AL2	MDA6
MAAA11	AU21	SA_MA[11]	SA_DQ[7]	AL1	MDA7
MAAA12	AT21	SA_MA[12]			
MAAA13	AW32	SA_MA[13]	SA_DQS[1]	AP3	DQSA1
MAAA14	AU20	SA_MA[14]	SA_DQS[1]	AP2	-DQSA1
MAAA15	AT20	SA_MA[15]			
-SWEA	AW29	SA_WE#	SA_DQ[8]	AN1	MDA8
-SCASA	AV30	SA_CAS#	SA_DQ[9]	AN4	MDA9
-SRASA	AU28	SA_RAS#	SA_DQ[10]	AR3	MDA10
			SA_DQ[11]	AR4	MDA11
SBA00	AY29	SA_BS[0]	SA_DQ[12]	AN2	MDA12
SBA01	AW28	SA_BS[1]	SA_DQ[13]	AN3	MDA13
SBA02	AV20	SA_BS[2]	SA_DQ[14]	AR2	MDA14
			SA_DQ[15]	AR1	MDA15
-CSA0	AU29	SA_CS#	SA_DQS[2]	AW4	DQSA2
-CSA1	AV32	SA_CS#	SA_DQS[2]	AW4	-DQSA2
-CSA2	AW30	SA_CS#			
-CSA3	AU33	SA_CS#			
CKEA0	AV19	SA_CKE[0]	SA_DQ[16]	AV2	MDA16
CKEA1	AT19	SA_CKE[1]	SA_DQ[17]	AV3	MDA17
CKEA2	AU18	SA_CKE[2]	SA_DQ[18]	AV5	MDA18
CKEA3	AV18	SA_CKE[3]	SA_DQ[19]	AW5	MDA19
			SA_DQ[20]	AU2	MDA20
MODT_A0	AV31	SA_ODT[0]	SA_DQ[21]	AJ3	MDA21
MODT_A1	AU32	SA_ODT[1]	SA_DQ[22]	AJ5	MDA22
MODT_A2	AU30	SA_ODT[2]	SA_DQ[23]	AY5	MDA23
MODT_A3	AW33	SA_ODT[3]			
			SA_DQS[3]	AV8	DQSA3
			SA_DQS[3]	AW8	-DQSA3
DCLKA0	AY25	SA_CK[0]	SA_DQ[24]	AY7	MDA24
-DCLKA0	AW25	SA_CK[0]	SA_DQ[25]	AU7	MDA25
DCLKA1	AU24	SA_CK[1]	SA_DQ[26]	AV9	MDA26
-DCLKA1	AU25	SA_CK[1]	SA_DQ[27]	AJ9	MDA27
DCLKA2	AW27	SA_CK[2]	SA_DQ[28]	AV7	MDA28
-DCLKA2	AY27	SA_CK[2]	SA_DQ[29]	AW7	MDA29
DCLKA3	AV26	SA_CK[3]	SA_DQ[30]	AW9	MDA30
-DCLKA3	AW26	SA_CK[3]	SA_DQ[31]	AY9	MDA31
			SA_DQS[4]	AV37	DQSA4
			SA_DQS[4]	AV36	-DQSA4
			SA_DQ[32]	AJ35	MDA32
			SA_DQ[33]	AW37	MDA33
			SA_DQ[34]	AJ39	MDA34
			SA_DQ[35]	AJ36	MDA35
			SA_DQ[36]	AW35	MDA36
			SA_DQ[37]	AY36	MDA37
			SA_DQ[38]	AJ38	MDA38
			SA_DQ[39]	AJ37	MDA39
			SA_DQS[5]	AP38	DQSA5
			SA_DQS[5]	AP39	-DQSA5
			SA_DQ[40]	AR40	MDA40
			SA_DQ[41]	AR37	MDA41
			SA_DQ[42]	AN38	MDA42
			SA_DQ[43]	AN37	MDA43
			SA_DQ[44]	AR39	MDA44
			SA_DQ[45]	AR38	MDA45
			SA_DQ[46]	AN39	MDA46
			SA_DQ[47]	AN40	MDA47
			SA_DQS[6]	AK38	DQSA6
			SA_DQS[6]	AK39	-DQSA6
			SA_DQ[48]	AL40	MDA48
			SA_DQ[49]	AL37	MDA49
			SA_DQ[50]	AJ38	MDA50
			SA_DQ[51]	AJ37	MDA51
			SA_DQ[52]	AL39	MDA52
			SA_DQ[53]	AL38	MDA53
			SA_DQ[54]	AJ39	MDA54
			SA_DQ[55]	AJ40	MDA55
			SA_DQS[7]	AF38	DQSA7
			SA_DQS[7]	AF39	-DQSA7
			SA_DQ[56]	AG40	MDA56
			SA_DQ[57]	AG37	MDA57
			SA_DQ[58]	AE38	MDA58
			SA_DQ[59]	AE37	MDA59
			SA_DQ[60]	AG39	MDA60
			SA_DQ[61]	AG38	MDA61
			SA_DQ[62]	AE39	MDA62
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DDR_0

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CPU-SK/1155/S/15

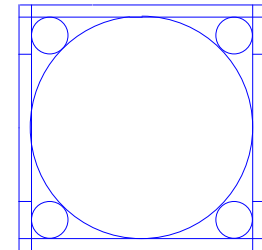
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MAAB2	AM19	SB_MA[2]			
MAAB3	AK18	SB_MA[3]			
MAAB4	AP19	SB_MA[4]	SB_DQ[0]	AG7	MDB0
MAAB5	AP18	SB_MA[5]	SB_DQ[1]	AG8	MDB1
MAAB6	AM18	SB_MA[6]	SB_DQ[2]	AJ9	MDB2
MAAB7	AL18	SB_MA[7]	SB_DQ[3]	AJ8	MDB3
MAAB8	AL17	SB_MA[8]	SB_DQ[4]	AG5	MDB4
MAAB9	AY17	SB_MA[9]	SB_DQ[5]	AG6	MDB5
MAAB10	AN23	SB_MA[10]	SB_DQ[6]	AJ6	MDB6
MAAB11	AU17	SB_MA[11]	SB_DQ[7]	AJ7	MDB7
MAAB12	AT18	SB_MA[12]			
MAAB13	AR26	SB_MA[13]	SB_DQS[1]	AM8	DQSB1
MAAB14	AY16	SB_MA[14]	SB_DQS[1]	AL8	-DQSB1
MAAB15	AV16	SB_MA[15]			
			SB_DQ[8]	AL7	MDB8
-SWEB	AR25	SB_WE#	SB_DQ[9]	AM7	MDB9
-SCASB	AK25	SB_CAS#	SB_DQ[10]	AM10	MDB10
-SRASB	AP24	SB_RAS#	SB_DQ[11]	AL10	MDB11
			SB_DQ[12]	AL6	MDB12
SBAB0	AP23	SB_BS[0]	SB_DQ[13]	AL9	MDB13
SBAB1	AM24	SB_BS[1]	SB_DQ[14]	AM9	MDB14
SBAB2	AW17	SB_BS[2]	SB_DQ[15]		
-CSB0	AN25	SB_CS#	SB_DQS[2]	AR8	DQSB2
-CSB1	AN26	SB_CS#	SB_DQS[2]	AP8	-DQSB2
-CSB2	AL25	SB_CS#			
-CSB3	AT26	SB_CS#			
CKEB0	AU16	SB_CKE[0]	SB_DQ[16]	AP7	MDB16
CKEB1	AY15	SB_CKE[1]	SB_DQ[17]	AR7	MDB17
CKEB2	AW15	SB_CKE[2]	SB_DQ[18]	AP10	MDB18
CKEB3	AV15	SB_CKE[3]	SB_DQ[19]	AP10	MDB19
			SB_DQ[20]	AP6	MDB20
MODT_B0	AL26	SB_ODT[0]	SB_DQ[21]	AR6	MDB21
MODT_B1	AP26	SB_ODT[1]	SB_DQ[22]	AP9	MDB22
MODT_B2	AM26	SB_ODT[2]	SB_DQ[23]	AR9	MDB23
MODT_B3	AK26	SB_ODT[3]			
			SB_DQS[3]	AN13	DQSB3
			SB_DQS[3]	AN12	-DQSB3
DCLKB0	AL21	SB_CK[0]	SB_DQ[24]	AM12	MDB24
-DCLKB0	AL22	SB_CK[0]	SB_DQ[25]	AM13	MDB25
DCLKB1	AK20	SB_CK[1]	SB_DQ[26]	AR13	MDB26
-DCLKB1	AK20	SB_CK[1]	SB_DQ[27]	AP13	MDB27
DCLKB2	AL23	SB_CK[2]	SB_DQ[28]	AL12	MDB28
-DCLKB2	AL23	SB_CK[2]	SB_DQ[29]	AL13	MDB29
DCLKB3	AP21	SB_CK[3]	SB_DQ[30]	AR12	MDB30
-DCLKB3	AN21	SB_CK[3]	SB_DQ[31]	AP12	MDB31
			SB_DQS[4]	AN29	DQSB4
			SB_DQS[4]	AN28	-DQSB4
			SB_DQ[32]	AR28	MDB32
			SB_DQ[33]	AP23	MDB33
			SB_DQ[34]	AL28	MDB34
			SB_DQ[35]	AL29	MDB35
			SB_DQ[36]	AP28	MDB36
			SB_DQ[37]	AP29	MDB37
			SB_DQ[38]	AM28	MDB38
			SB_DQ[39]	AM29	MDB39
			SB_DQS[5]	AP33	DQSB5
			SB_DQS[5]	AR33	-DQSB5
			SB_DQ[40]	AP32	MDB40
			SB_DQ[41]	AP21	MDB41
			SB_DQ[42]	AP35	MDB42
			SB_DQ[43]	AP34	MDB43
			SB_DQ[44]	AR32	MDB44
			SB_DQ[45]	AR31	MDB45
			SB_DQ[46]	AR35	MDB46
			SB_DQ[47]	AR34	MDB47
			SB_DQS[6]	AL33	DQSB6
			SB_DQS[6]	AM33	-DQSB6
			SB_DQ[48]	AM32	MDB48
			SB_DQ[49]	AL35	MDB49
			SB_DQ[50]	AL32	MDB50
			SB_DQ[51]	AM34	MDB51
			SB_DQ[52]	AL31	MDB52
			SB_DQ[53]	AM35	MDB53
			SB_DQ[54]	AL34	MDB54
			SB_DQ[55]	AL34	MDB55
			SB_DQS[7]	AG35	DQSB7
			SB_DQS[7]	AG34	-DQSB7
			SB_DQ[56]	AH35	MDB56
			SB_DQ[57]	AH34	MDB57
			SB_DQ[58]	AE34	MDB58
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			SB_DQ[60]	AJ35	MDB60
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			SB_DQ[63]	AF35	MDB63

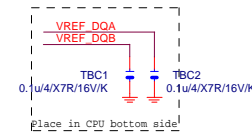
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2 OF 10

CPU-SK/1155/S/15

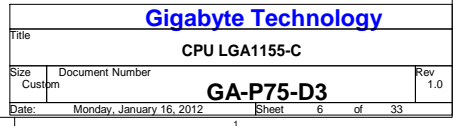
LGA1155
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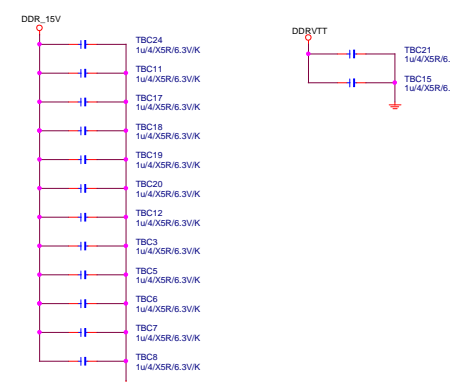
Need check the new CPU ME



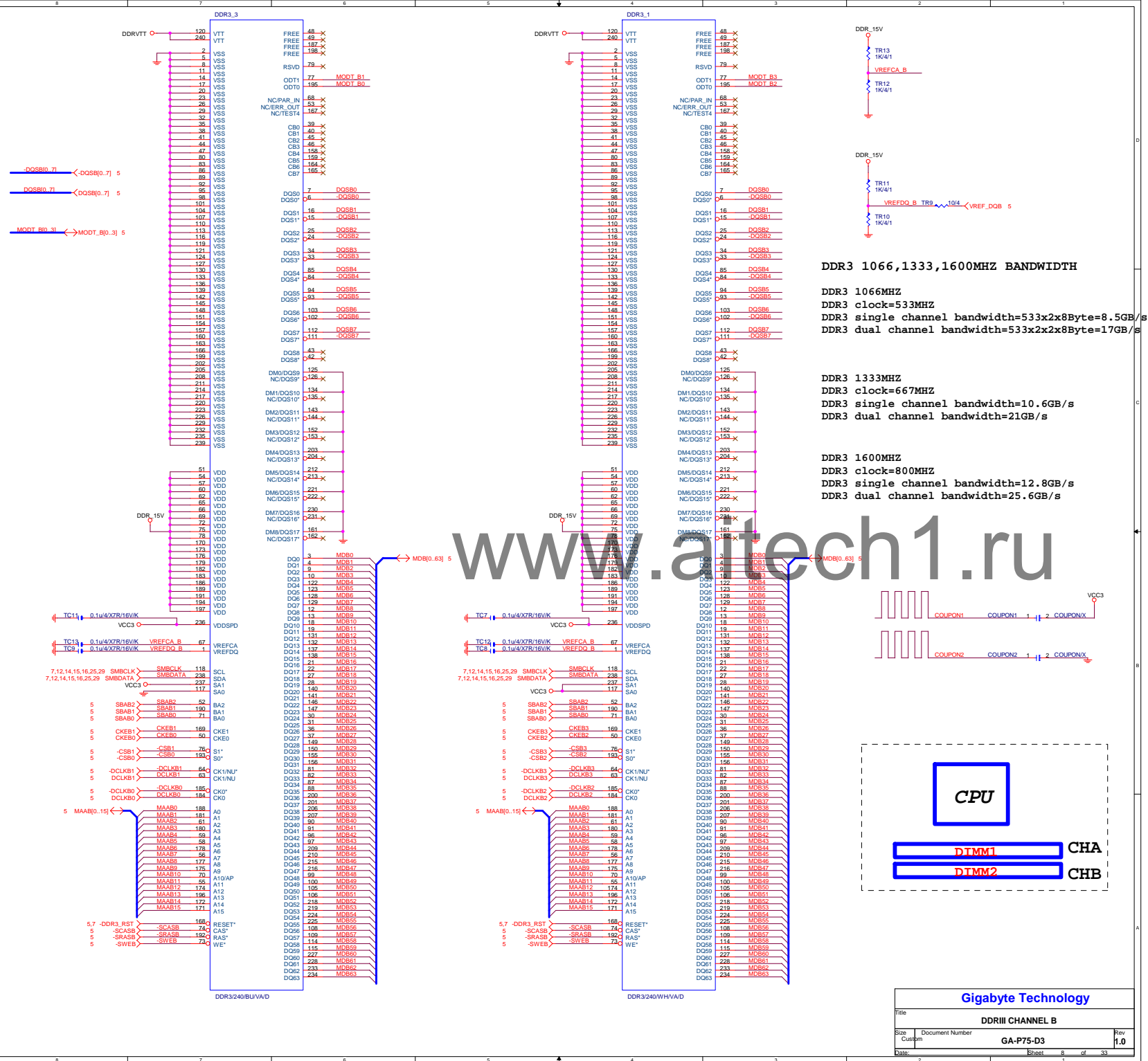
Gigabyte Technology

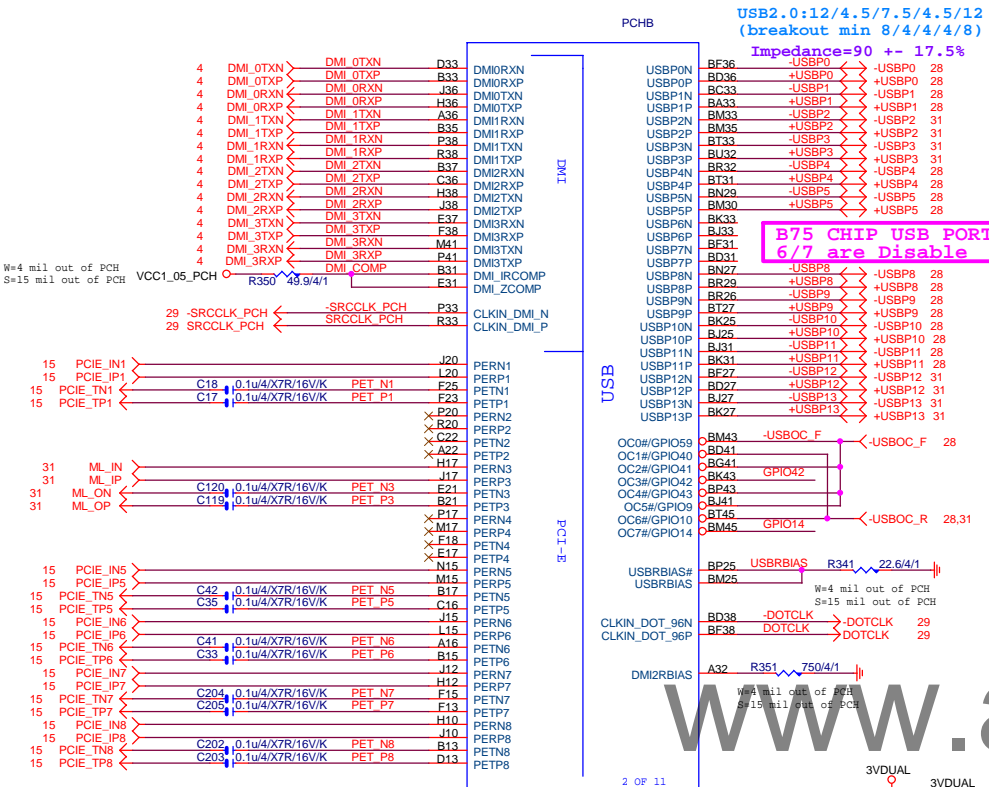
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Size			Document Number		
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Date:			Monday, January 16, 2012		
Sheet			5 of 33		
Rev			1.0		





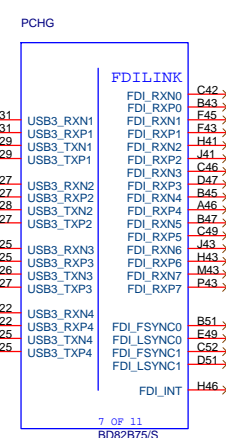
Gigabyte Technology			
Title DDRIII CHANNEL A			
Size Custom	Document Number GA-P75-D3	Rev 1.1	
Date:	Sheet 7 of 33		





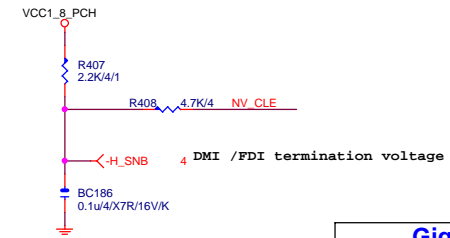
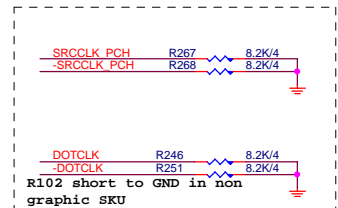
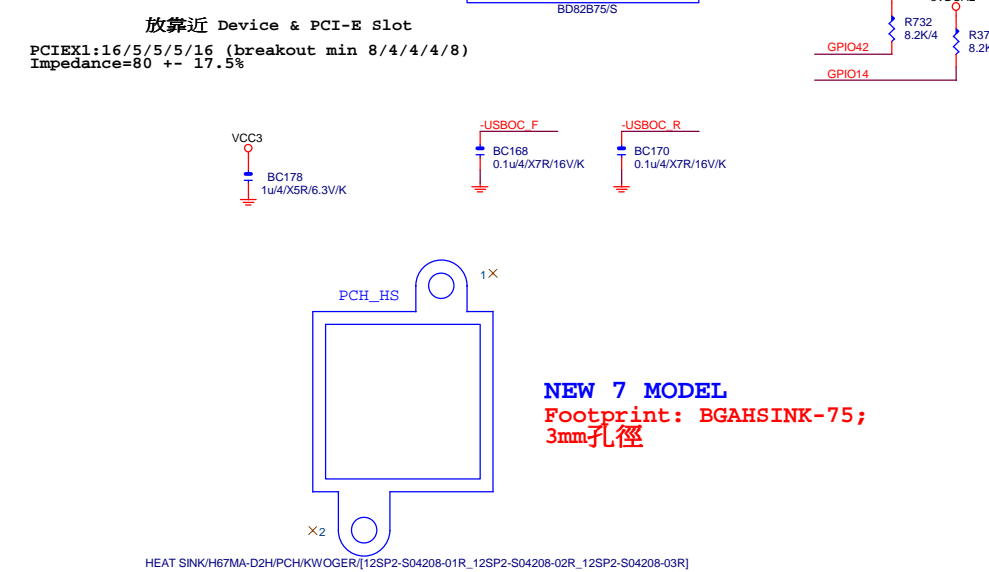
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(breakout min 8/4/4/4/8)
Impedance=90 +- 17.5%

USB3.0:20/5/7/5/20 (breakout min 8/4/4/4/8) ; ONLY 3 VIAS
Impedance=85 +- 17.5%
Back Panel < 10000 MILS
Front Panel < 6000 MILS

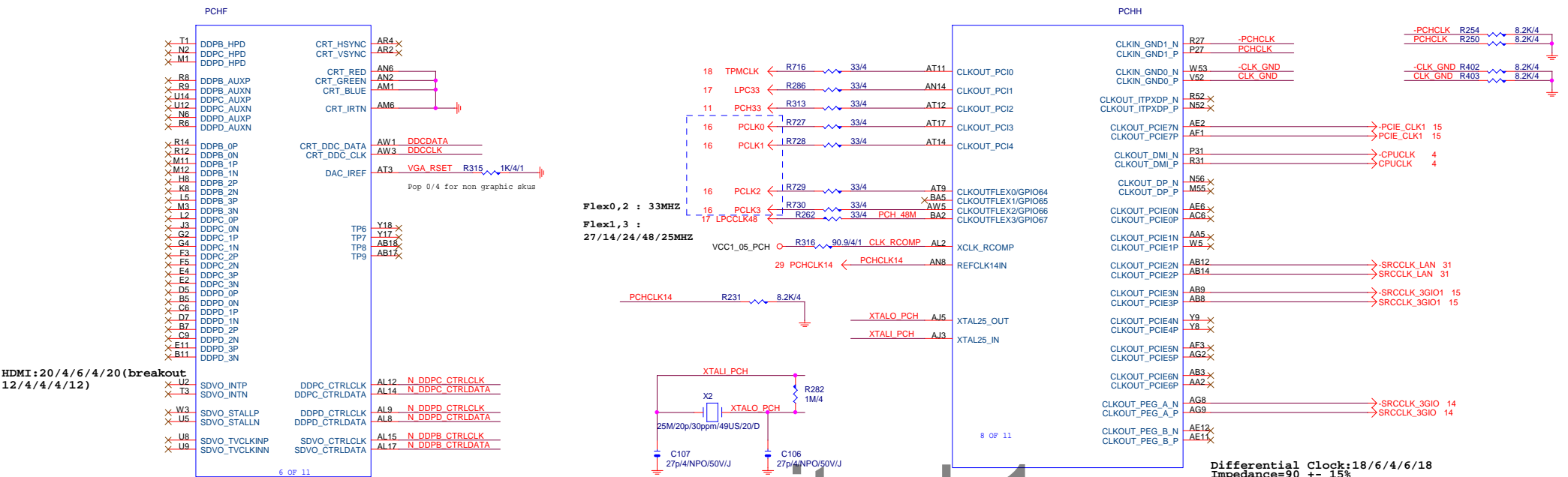


OC[3:0]# for Device 29 (ports 0-7)
OC[7:4]# for Device 26 (ports 8-13)

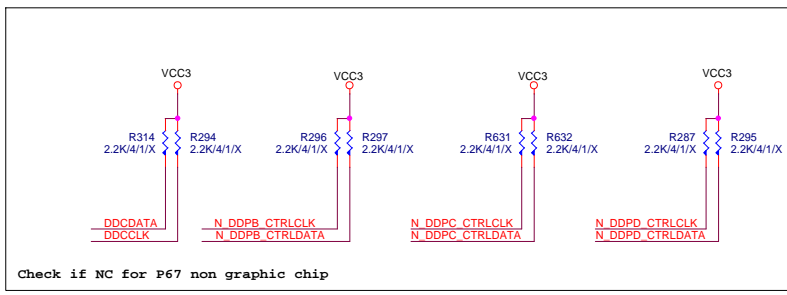
USB OC# Configure	
OC0#	USB0,1
OC1#	USB2,3
OC2#	USB4,5
OC3#	USB6,7
OC4#	USB8,9
OC5#	USB10,11
OC6#	USB12,13
OC7#	Not Use

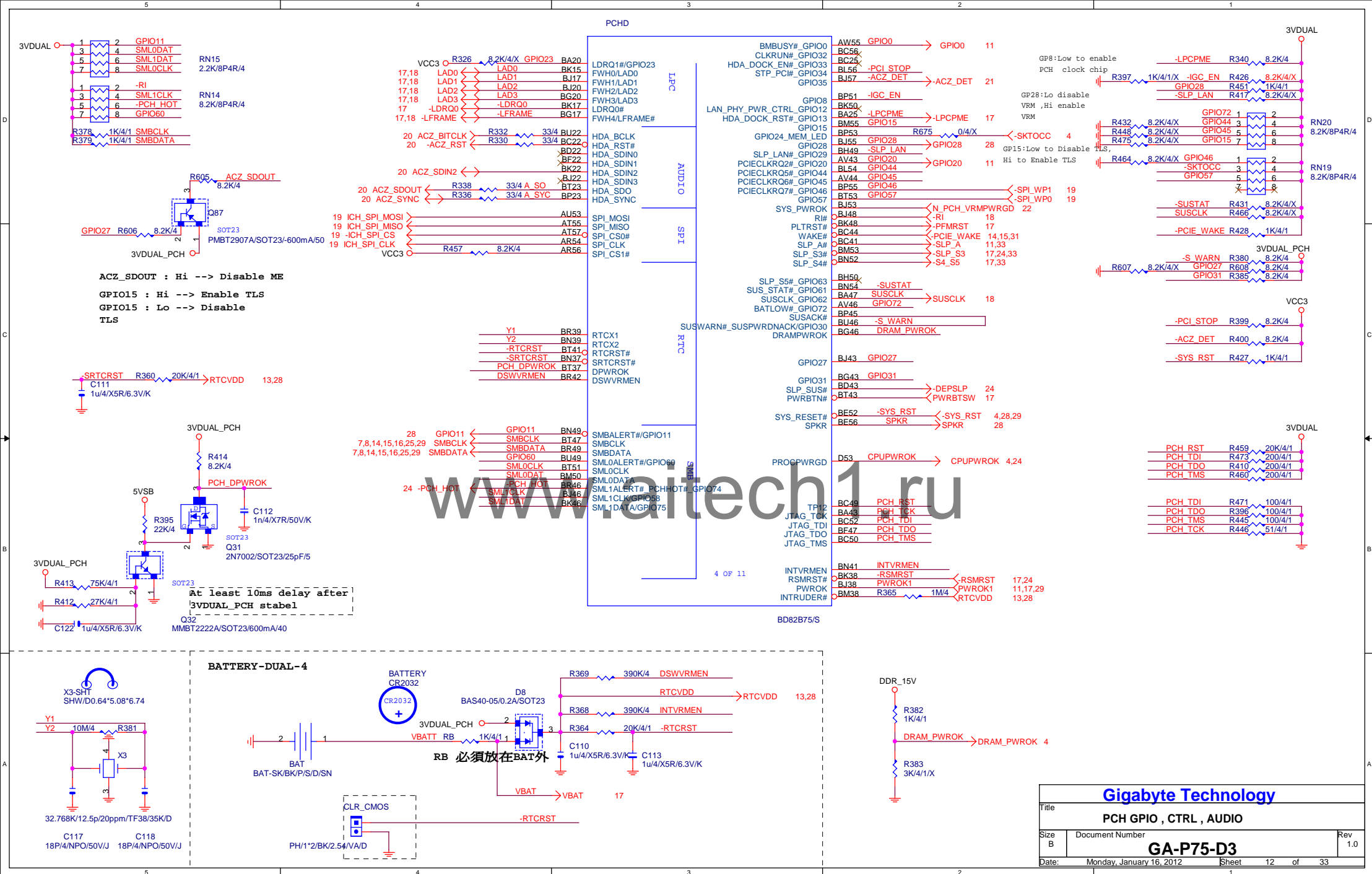


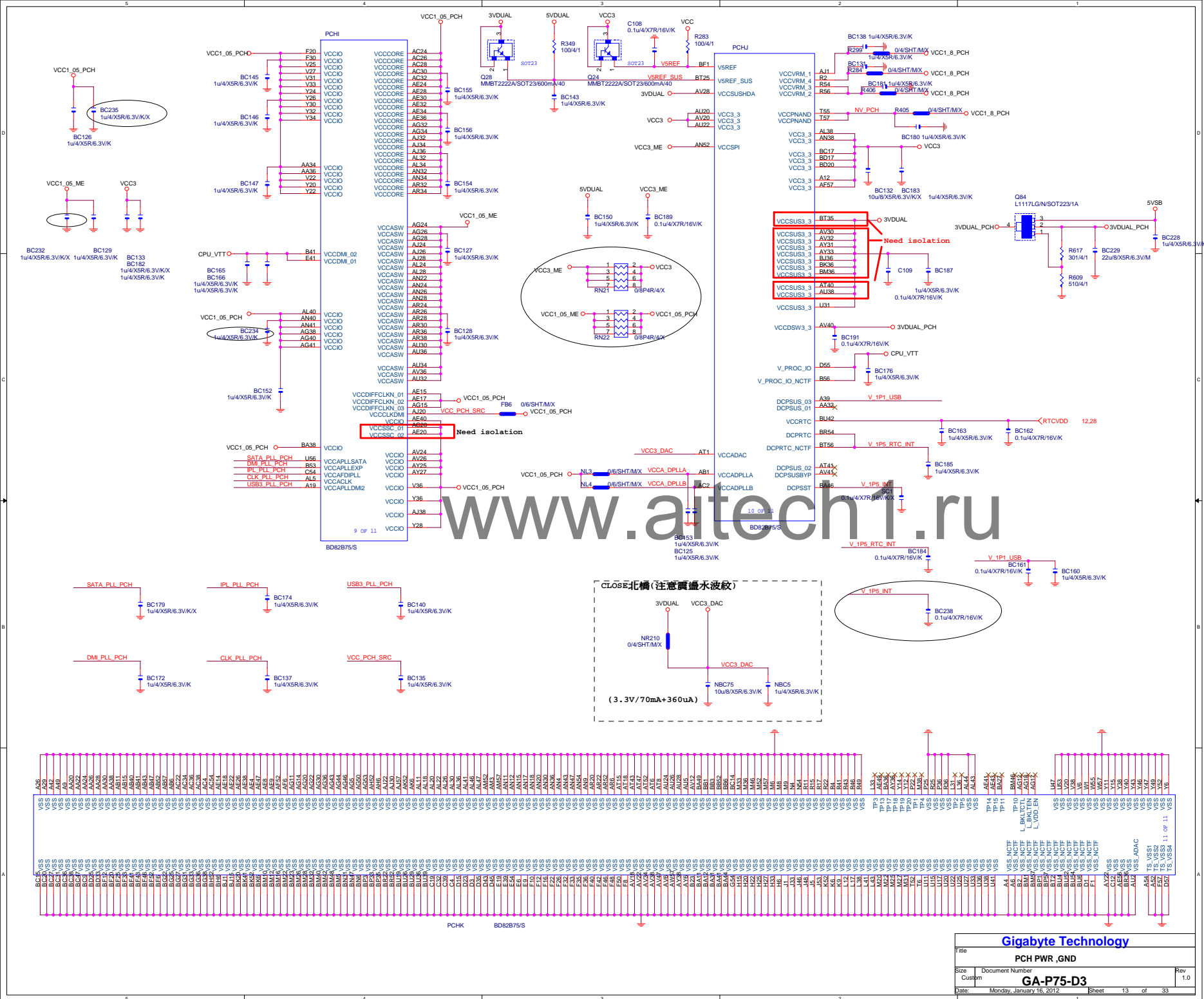
Gigabyte Technology			
Title			
PCH FDI,DMI,USB,PCI-E			
Size	Document Number	Rev	
Custom		GA-P75-D3	
Date:	Monday, January 16, 2012	Sheet	9 of 33

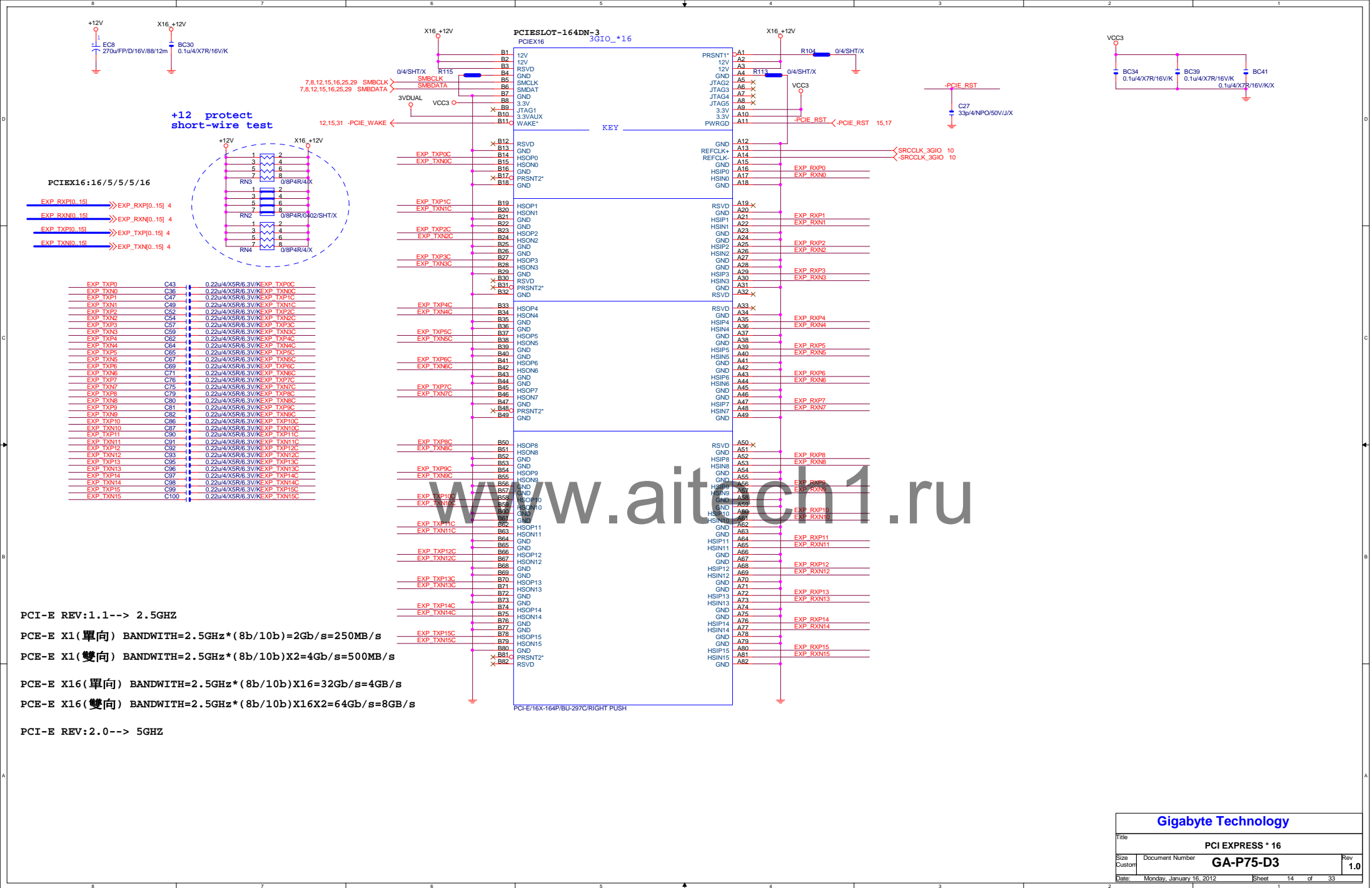


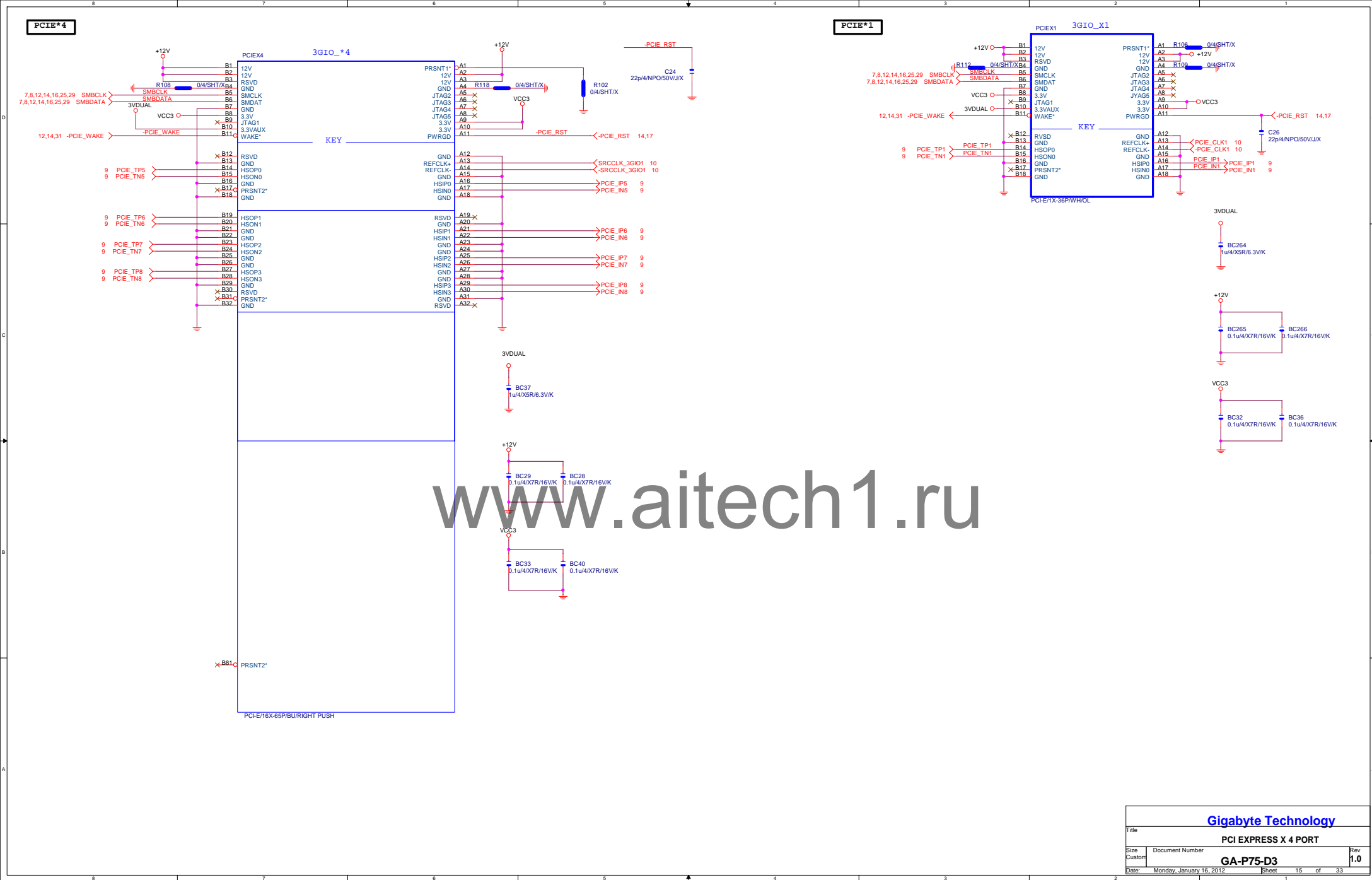
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Impedance=90 +/- 15%

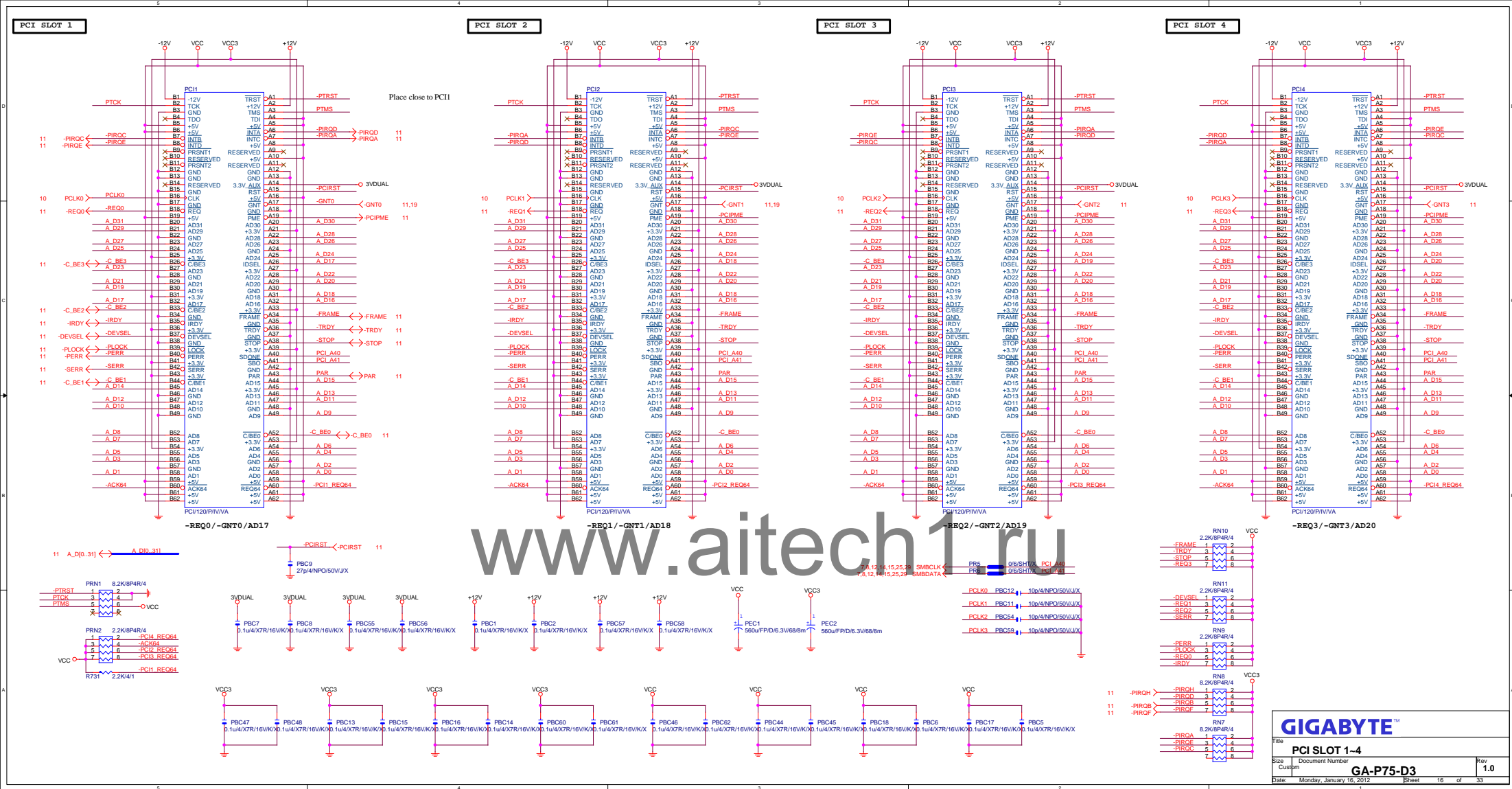


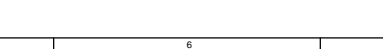
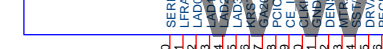
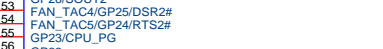
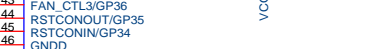
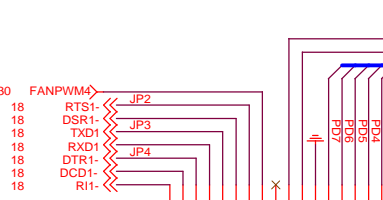
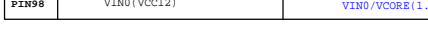
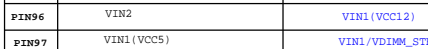
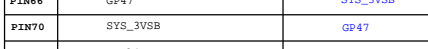
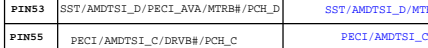
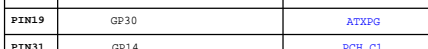
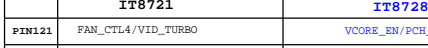
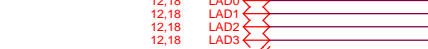
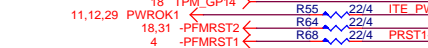
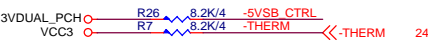
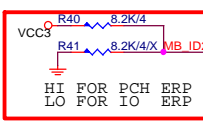












IT8728F(GB)

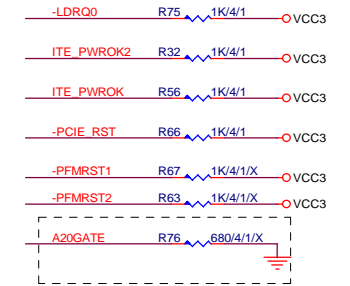
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	IT8721	IT8728
PIN121	FAN_CTL4/VID_TURBO	VCORE_EN/PCH_C0
PIN120	VDDA_EN	VLDI_EN/PCH_D0
PIN19	GP30	ATXPG
PIN31	GP14	PCH_C1
PIN53	SST/AMDTSI_D/PECI_AVA/MTRB#/PCH_D	SST/AMDTSI_D/MTRB#/PCH_D1
PIN55	PECI/AMDTSI_C/DRV8#/PCH_C	PECI/AMDTSI_C/DRV8#
PIN66	GP47	SYS_3VSB
PIN70	SYS_3VSB	GP47
PIN95	VIN3/ATXPG	VIN2(VCC5)
PIN96	VIN2	VIN1(VCC12)
PIN97	VIN1(VCC5)	VIN1/VDIMM_STR(1.5V)
PIN98	VIN0(VCC12)	VIN0/VCORE(1.1V)

DUAL BIOS OPT STRAP

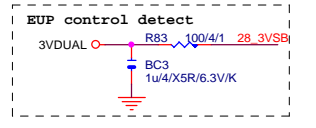
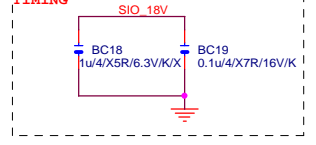


Only For Push-Pull Mode



Hi :Disable WDT
Lo :Enable WDT to rest PWROK

internal power pin, max 22nF cap---->2uf for 8728 低温相容性---->ITE-用程式卡下不良.
FOR EX 改用1顆0.1u 修正VCORE / 8VDUAL



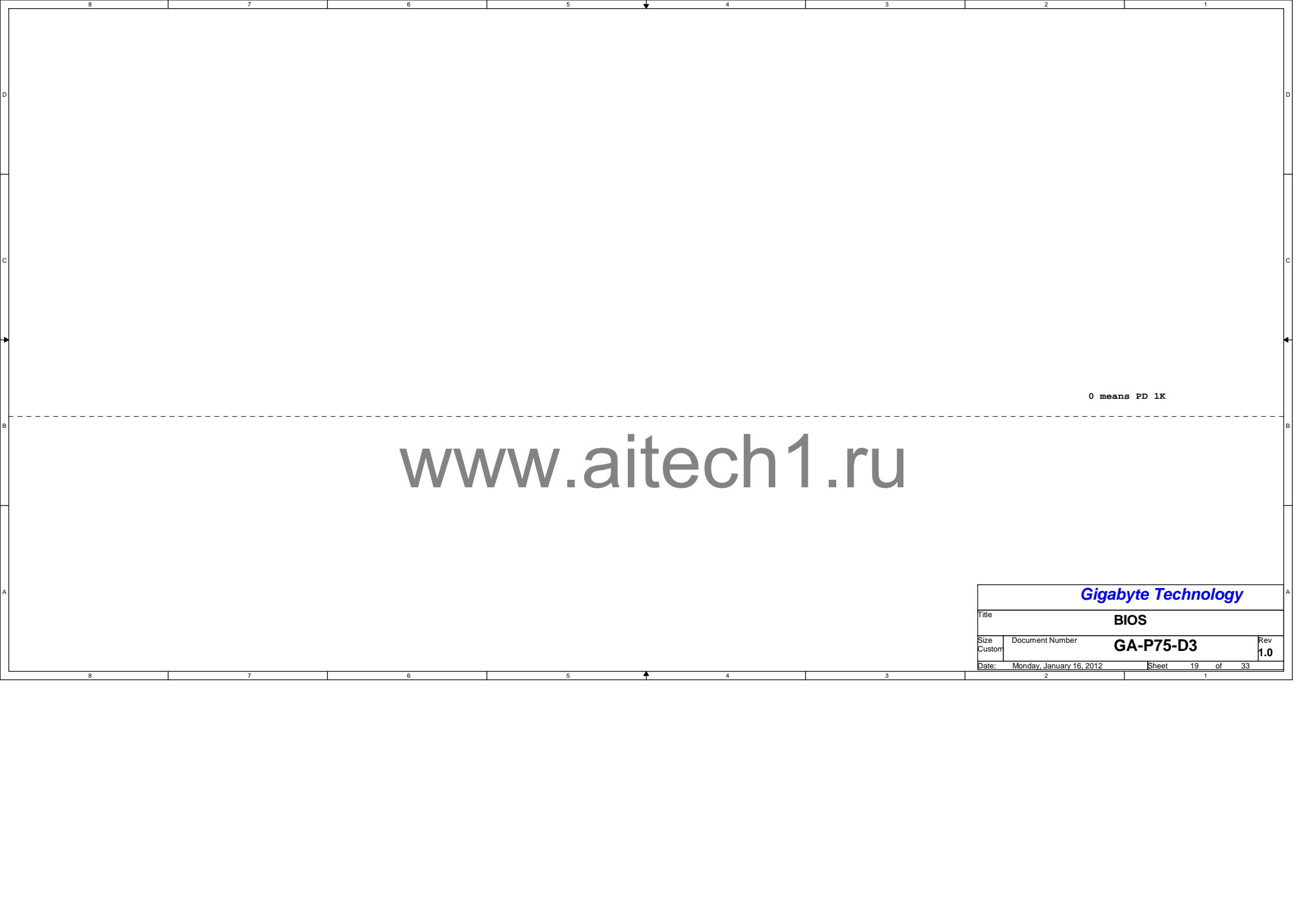
JP4	1	k8 power sequency function is Disable
JP4	0	k8 power sequency function is Enable
JP3	1 1	The default value of EC Index 63h/6Bh/73h is 80h.
JP5	1 0	The default value of EC Index 63h/6Bh/73h is FFh.
JP5	0 1	The default value of EC Index 63h/6Bh/73h is 00h.
JP5	0 0	The default value of EC Index 63h/6Bh/73h is 40h.

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ITE 8728 LPC IO

GA-P75-D3

Rev 1.0



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0 means PD 1K

Gigabyte Technology			
Title		BIOS	
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	GA-P75-D3		1.0
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	2		1

CR26: 20K/4/0.1% @ALC889A
CR26: 20K/4/1% @others



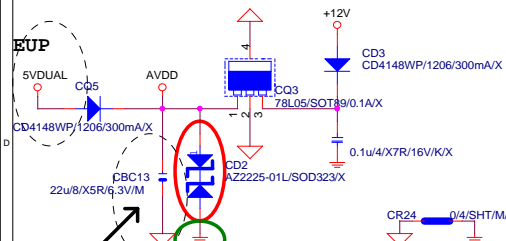
Title	HD AUDIO ALC887-VD2
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Size Custom	Document Number GA-P75-D3
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Date: Monday, January 16, 2012 Sheet 20 of 33

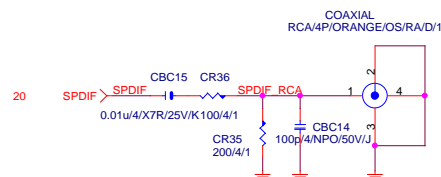
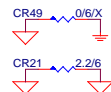
Rev
1.0

CODEC POWER/EMI PAD

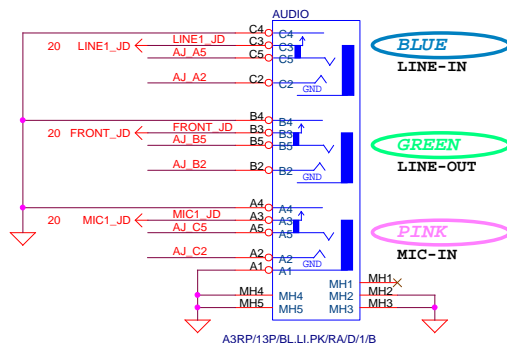
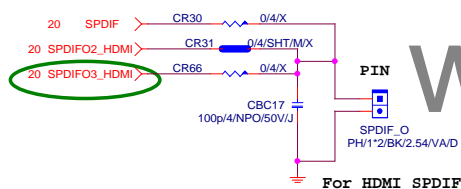


上ALC892時,此顆電容要保留

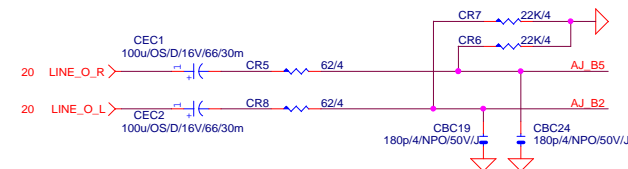
ADD CD2 For ESD PROTECT DIODE



SPDIF_OUT

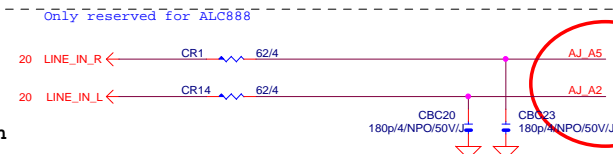
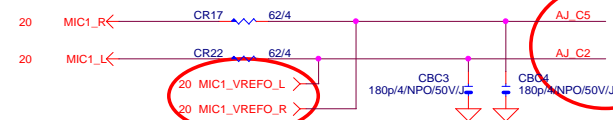


LINE-OUT



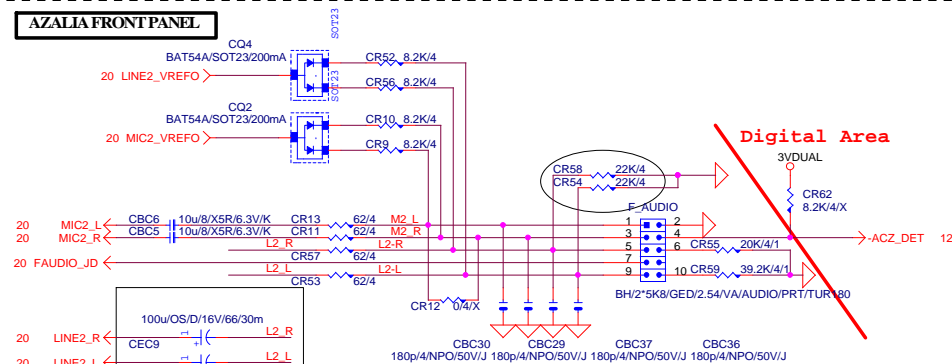
LINE-IN

Verify MIC function
in LINE-in

**MIC-IN**

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AZALIA FRONT PANEL

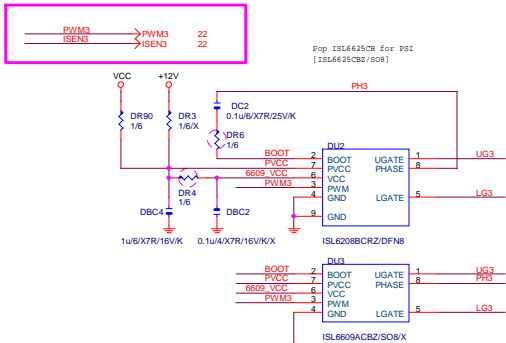


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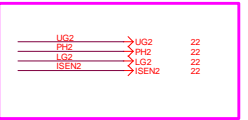
Title			
AUDIO JACK			
Size Custom	Document Number		Rev
	GA-P75-D3		1.0
Date:	Monday, January 16, 2012	Sheet	21 of 33



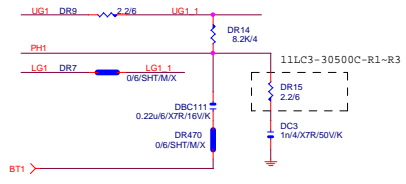
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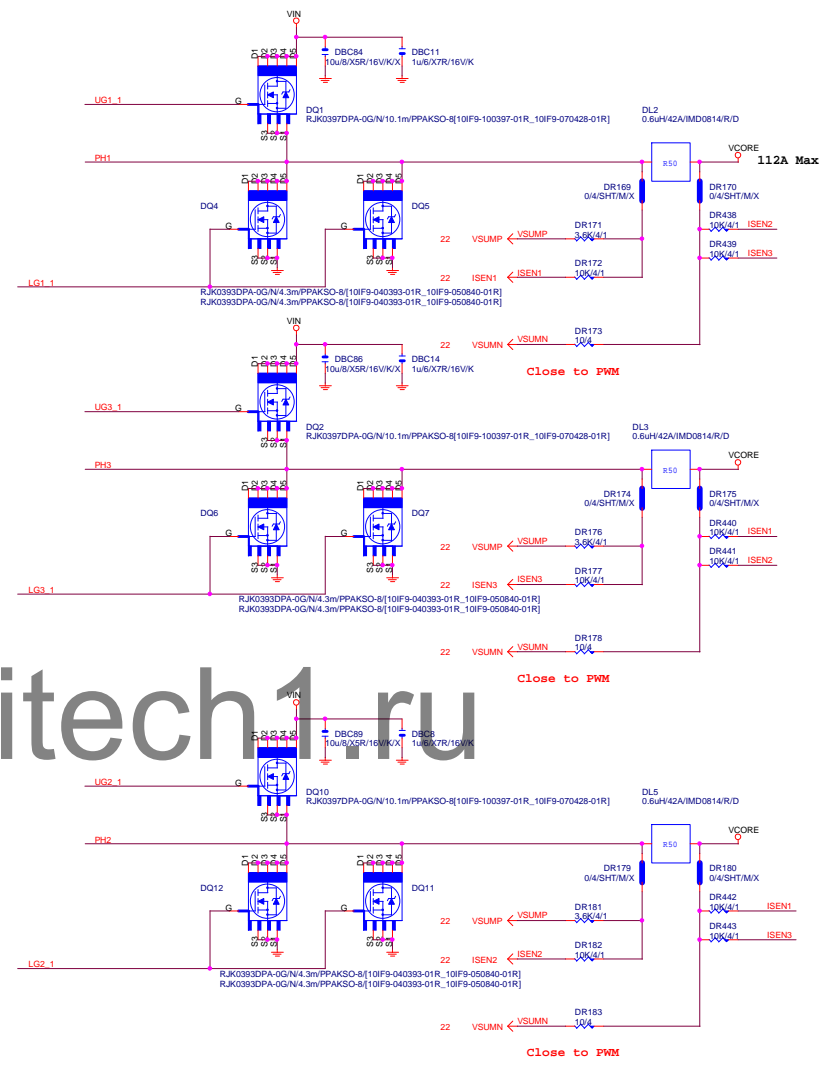
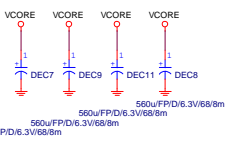
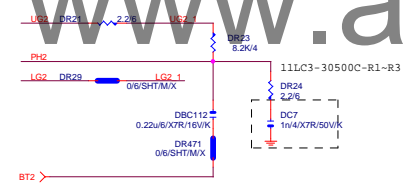
6609 colay with 6208



[3]

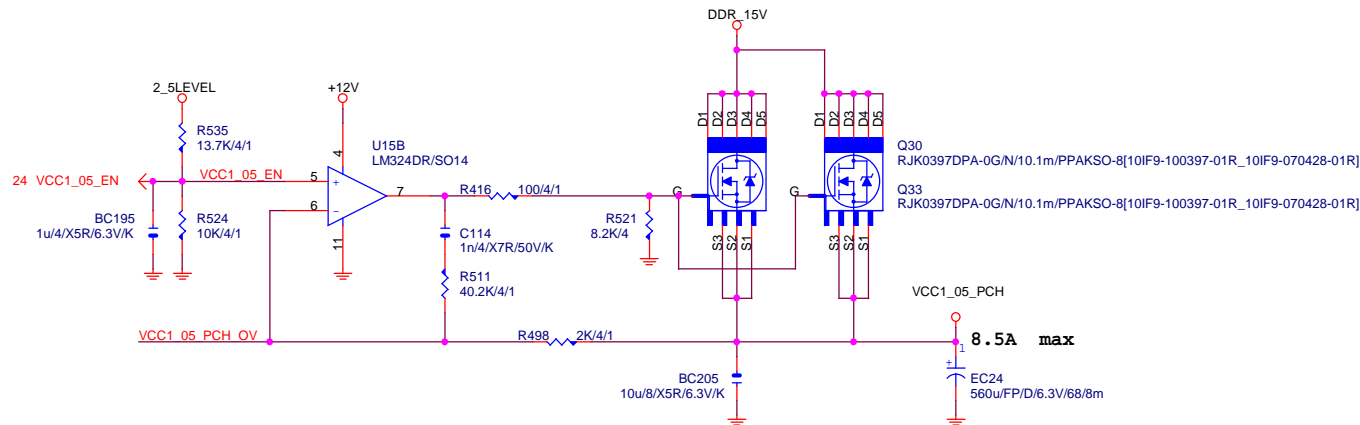


[2]



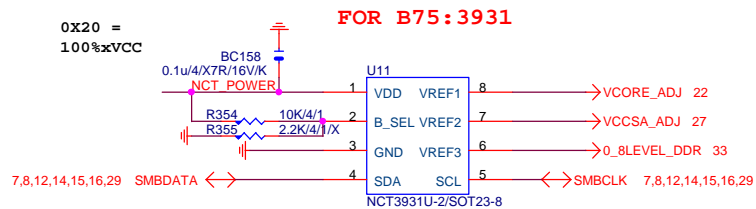
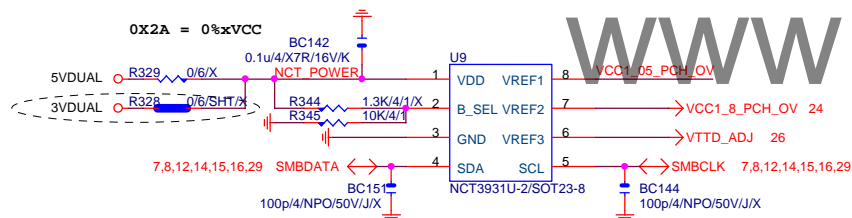
Gigabyte Technology			
File	VCORE_ISL95836_2		
Size	Document Number	GA-P75-D3	Rev 1.0
Custom			
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VCC1_05_PCH



Voltage console

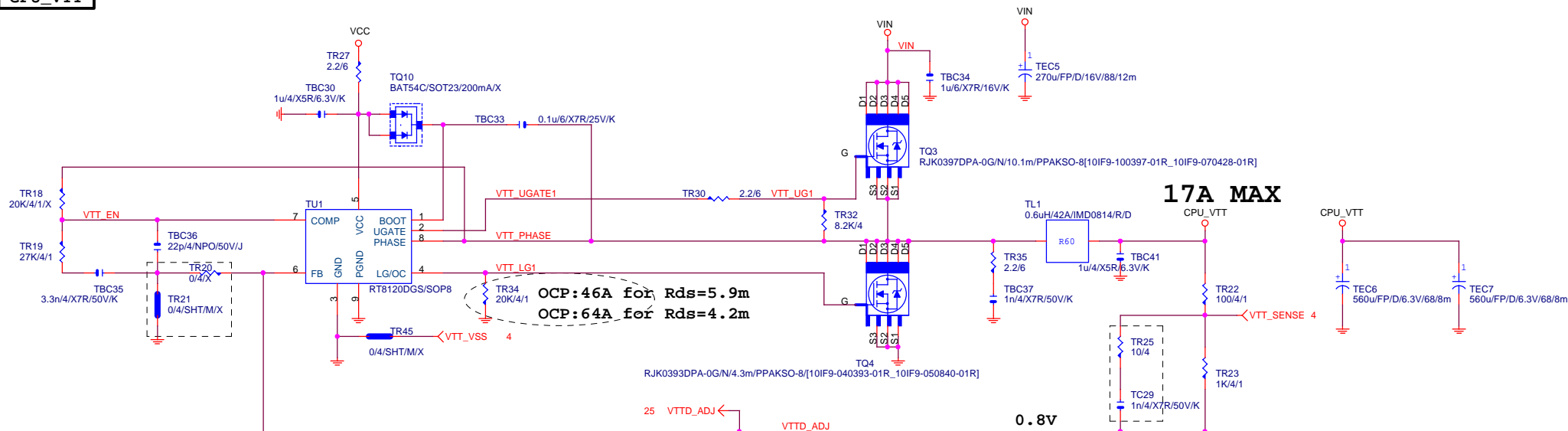
ADDRESS	0X2A	0X20	0X22	0X26
R1 (K)	OPEN	10	1.3	3
R2 (K)	10	OPEN	3.9	2.2
%VCC	0	100	75	42



up6262	0X2A	0X20
VREF1	VCC1_05_PCH	VCORE
VREF2	VCC1_8_PCH	VCCSA
VREF3	CPU_VTT	DDR

Gigabyte Technology		
PCH CORE / VOLTAGE CONSOLE		
Size B	Document Number	Rev
	GA-P75-D3	1.0
Date:	Monday, January 16, 2012	Sheet 25 of 33

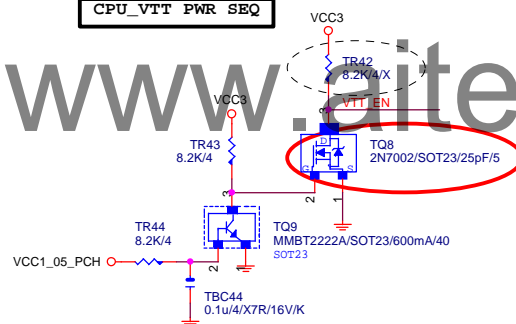
CPU_VTT



$$OCP:46A=Ro_{set} \cdot I_{ocset} / R_{ds(on)}$$

$$=27K \cdot 10\mu A / 5.9m$$

CPU_VTT PWR SEQ



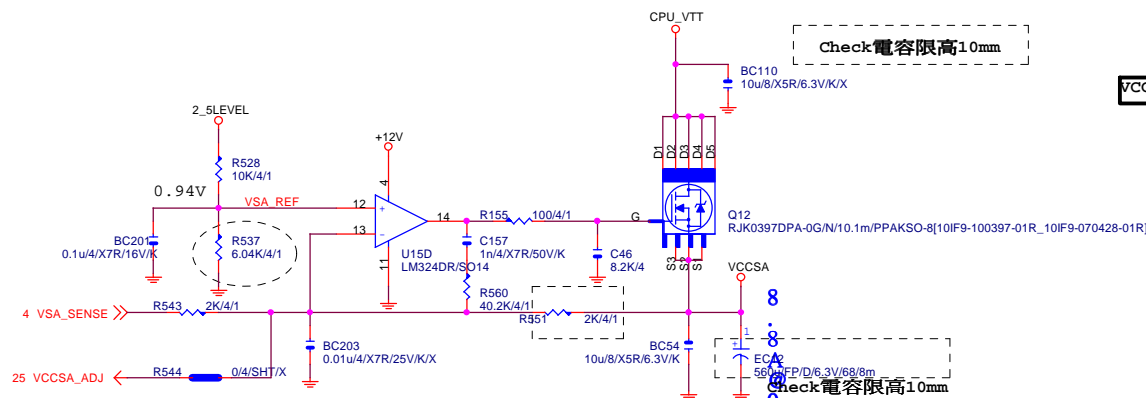
VTT_SEL	
HI	1.05V
LO	1.0V

According intel
CDI/IBP#476733, 固定1.05v

GIGABYTE

Title			RT8120 CPU_VTT
Size	Document Number	Rev	
Custom	GA-P75-D3	1.0	
Date:	Monday, January 16, 2012	Sheet	26 of 33

VCC_SA



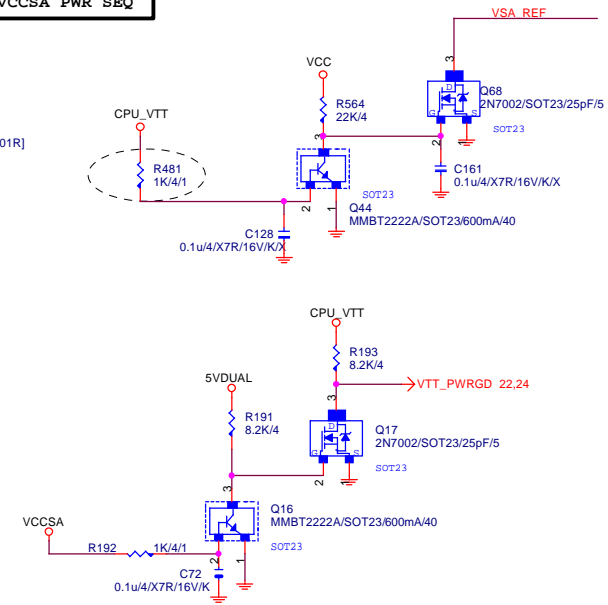
PDG 1.01

VSA_SEL	0.85V
HI	0.925V
LO	0.925V

According intel
CDI/IBP#476733, 固定0.925V

8
8
5
/ 0
9
2
5
V

VCCSA PWR SEQ

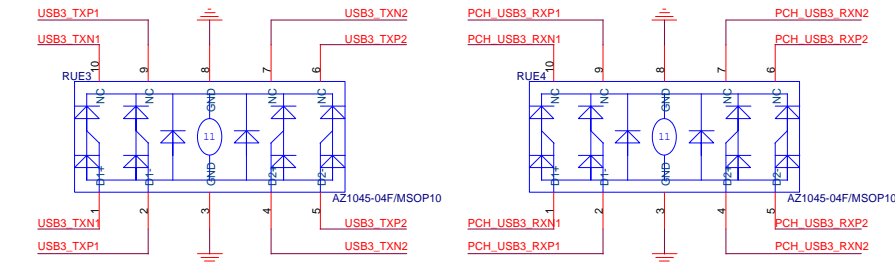
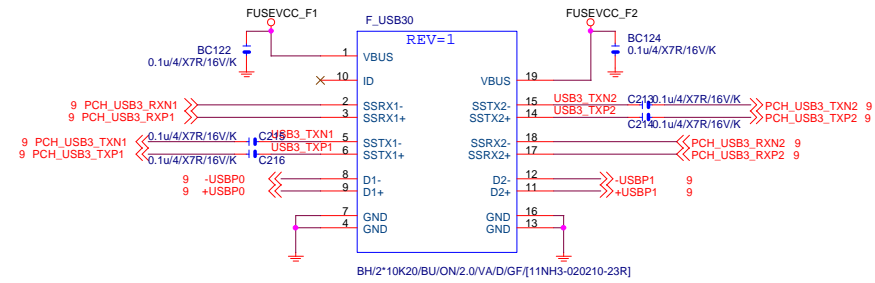


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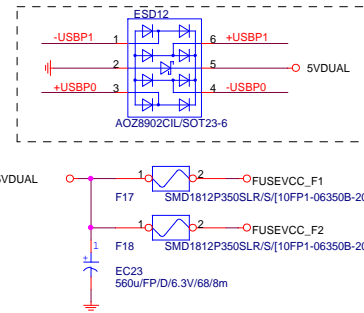
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VCCSA POWER		
Size	Document Number	Rev
Custom	GA-P75-D3	1.0
Date:	Monday, January 16, 2012	Sheet 27 of 33

FRONT USB1

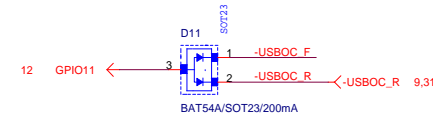


ESD Close to connector

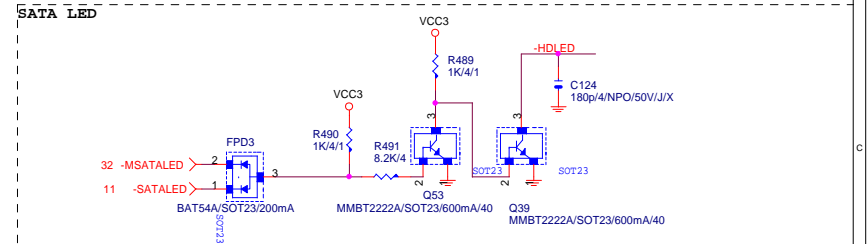
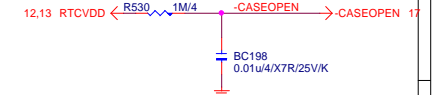
ESD Close to connector



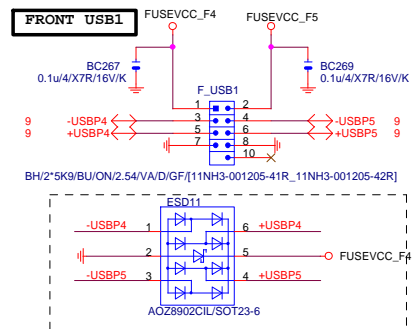
F_USB POWER PROTECT



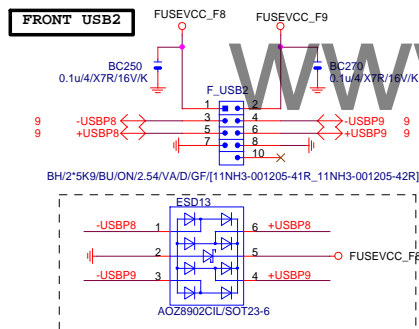
CASE OPEN



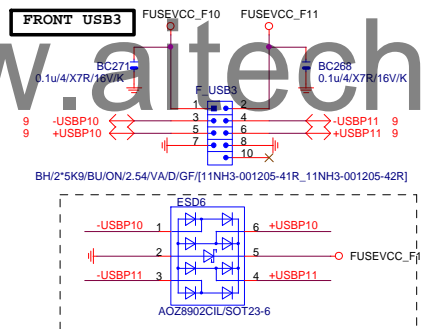
[SATA LED]



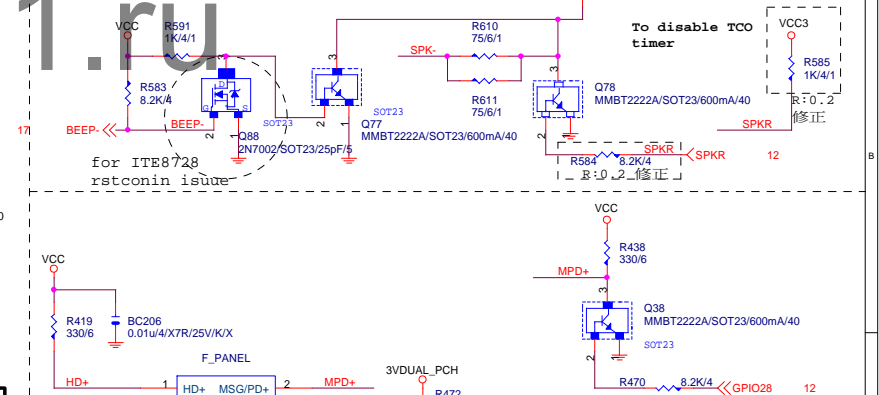
ESD Close to connector



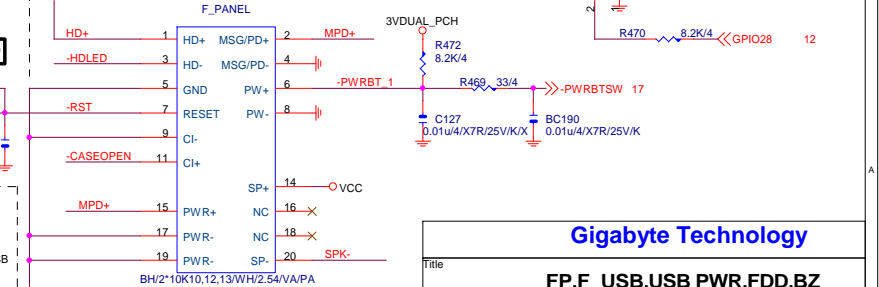
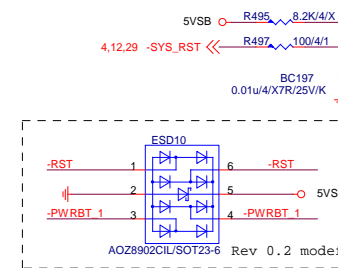
ESD Close to connector



ESD Close to connector



INTEL FRONT PANEL

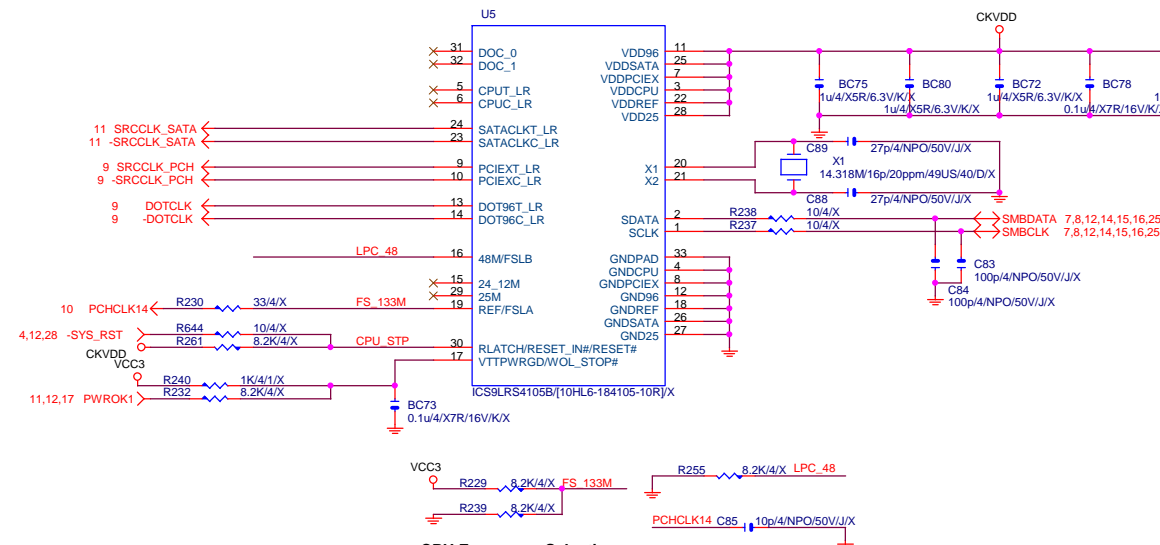
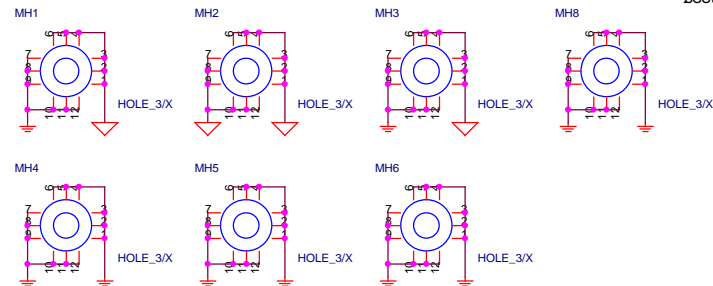


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FP,F_USB,USB PWR,FDD,BZ

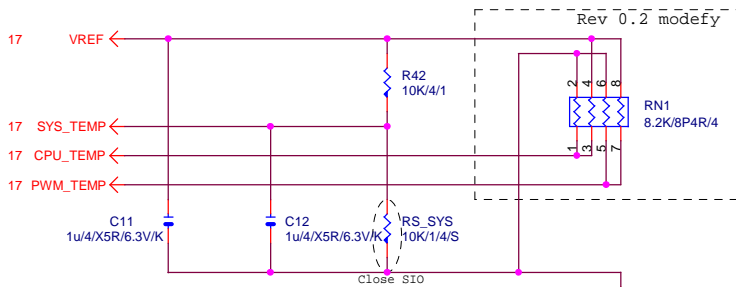
Size Custom	Document Number	GA-P75-D3	Rev 1.0
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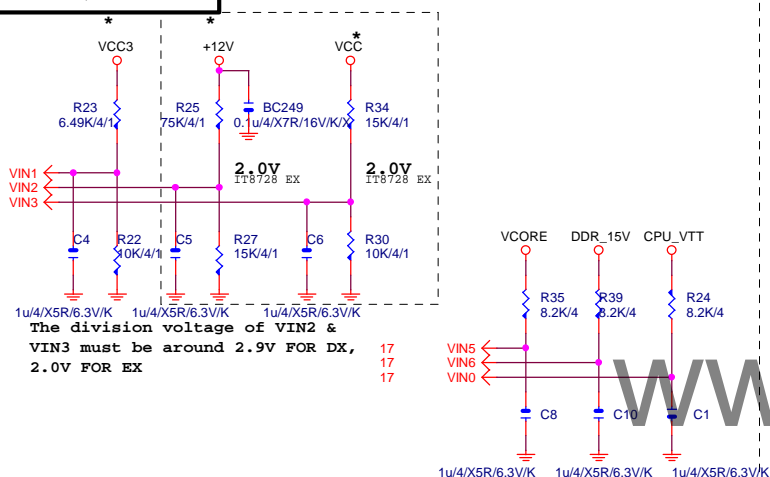


Gigabyte Technology			
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ATX POWER CONNECTOR			
Size Custom	Document Number	GA-P75-D3	Rev 1.0
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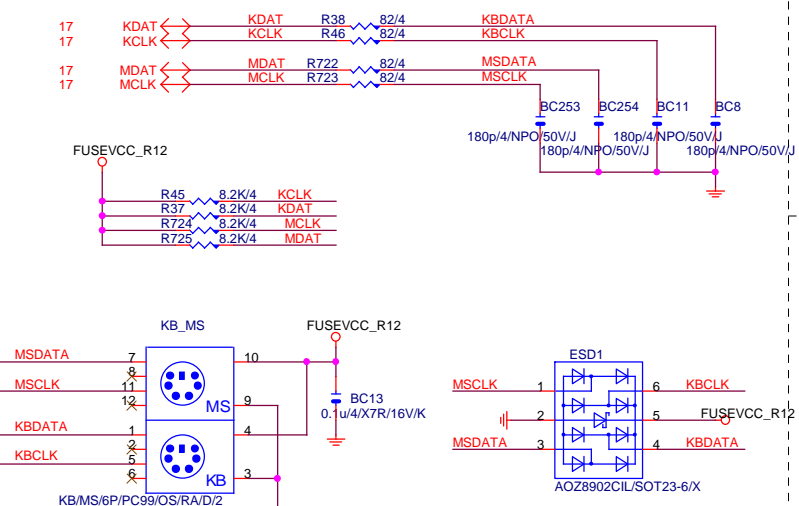
TEMP H/W MONITOR



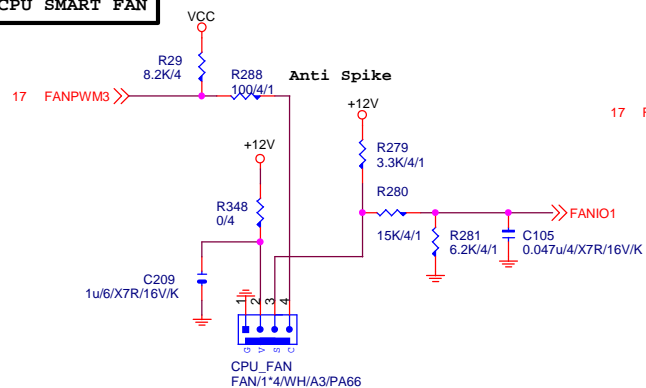
VOLTAGE-- H/W MONITOR



KB/USB

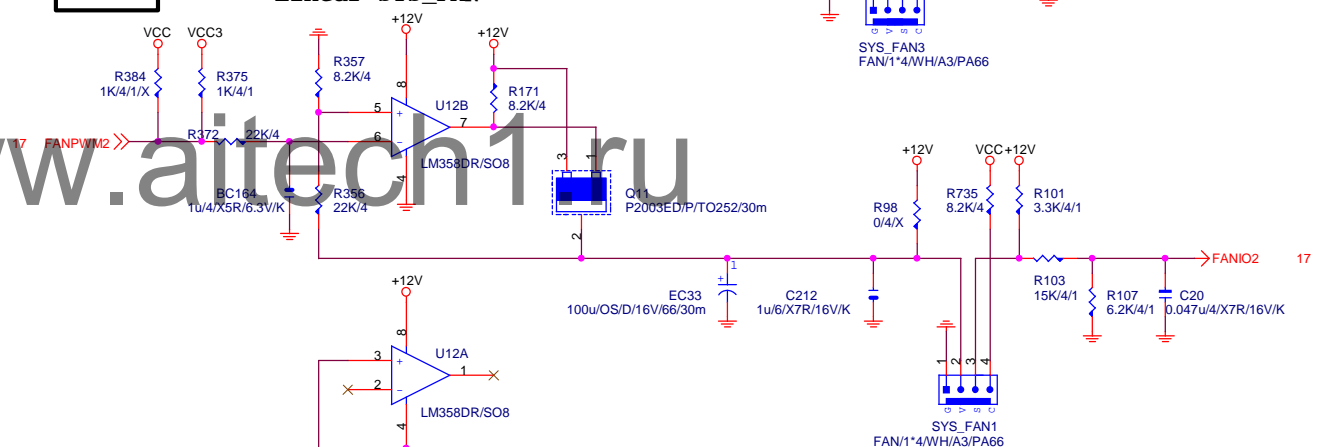


CPU SMART FAN

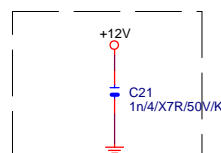
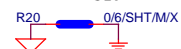


SYS FAN

Linear SYS_FAN



FOR EMI ONLY



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HWM,KB/MS, FAN CTRL

Size	Document Number
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GA-P75-D3

Rev

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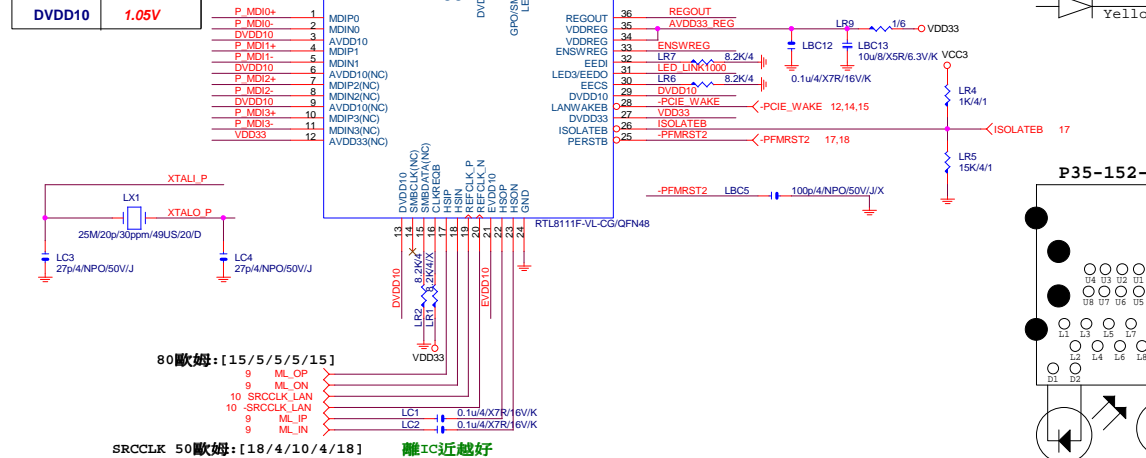
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3	100
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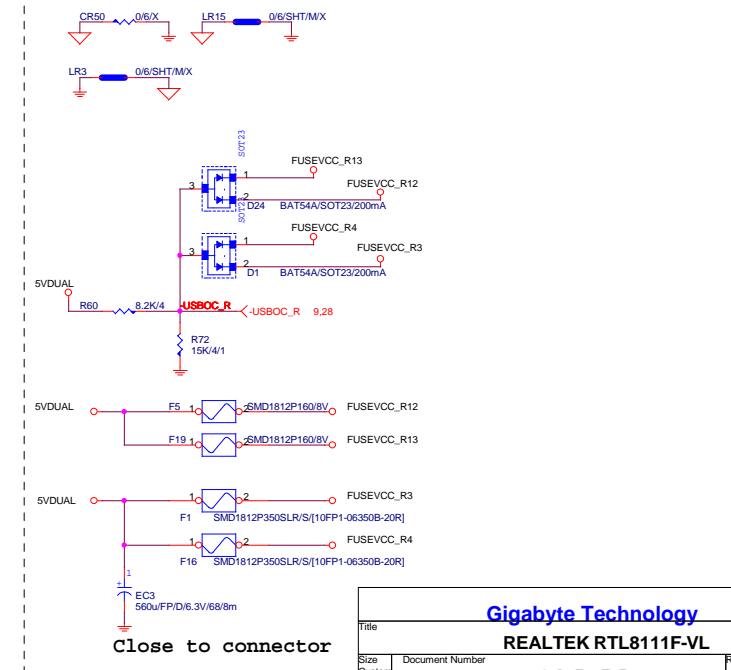
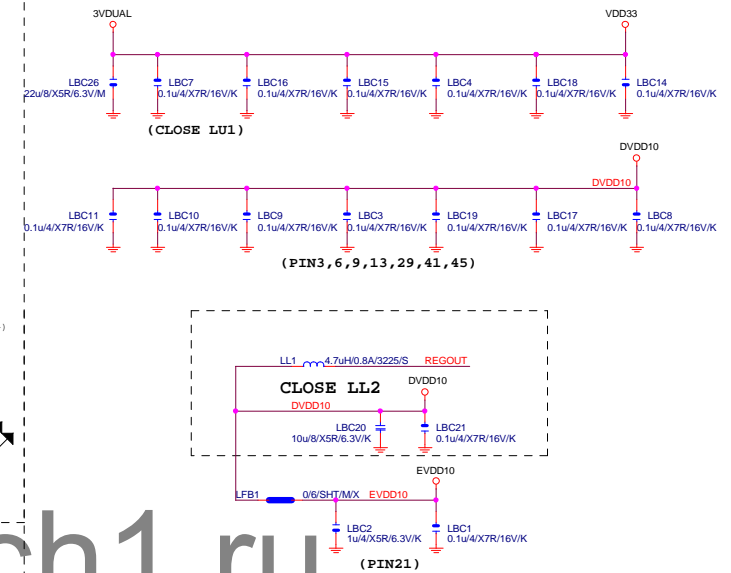
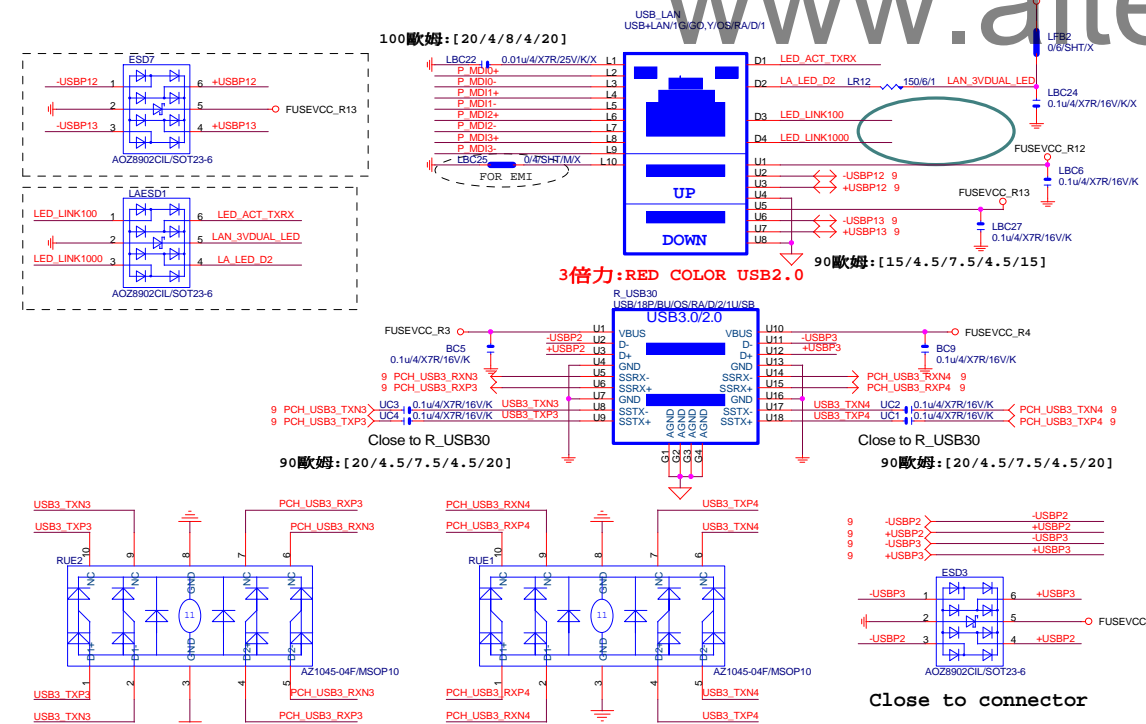
PCIE-1G LAN

Power domain chart

	RTL8111E
AVDD33	3.3V
DVDD33	3.3V
VDDREG	3.3V
DVDD10	1.05V



USB30_LAN CONNECTOR

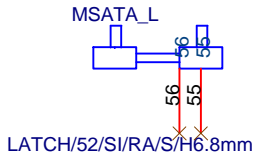
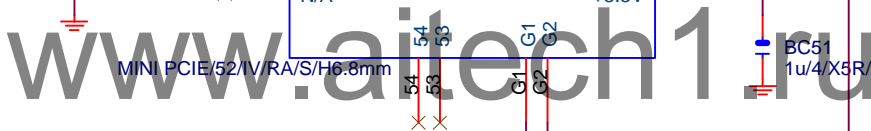


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REALTEK RTL8111F-VL

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MSATA



GIGABYTE™

Title

MSATA

Size
A

Document Number

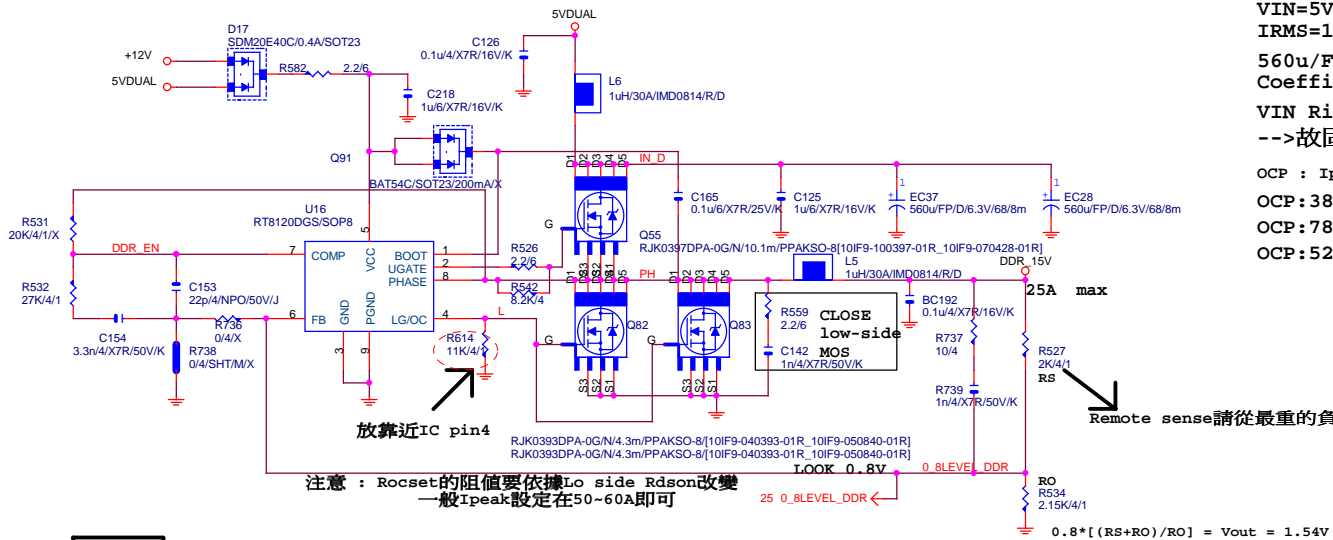
GA-P75-D3

Rev	1.
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DDR18V



VIN=5V, VOUT=1.5V, IOUT=25A, PHASE=1
IRMS=11.45A
560u/FP/D/6.3V/68/8m RIPPLE CURRENT=4.7A
Coefficient=1.7(85°C), 1(105°C)
VIN Ripple current=4.7X1.7=7.99A(85°C)
-->故固態電容須2X7.99=15.98>11.45A

OCP : $I_{peak} = (2 \times I_{ocset} \times R_{ocset}) / R_{dson}$
OCP: 38.31A for Rds=6.7m for vishay@4.5V
OCP: 78.78A for Rds=3.3m for renesas@10V
OCP: 52A=Roaset*Iocset / Rds(on)
=13K*10uA / [5//5]

PWR SEQ

